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REMINDER

COPY

Applicant: Junichi YANO, et al. Docket No. 60188-126

Title: INTEGRATED CIRCUIT WITH CPU AND FPGA FOR RESERVED INSTRUCTIONS EXECUTION WITH CONFIGURATION DIAGNOSIS (As Amended)

Date Sent: 12/1/2004 ☒ Hand Carried ☐ Fax ☐ Electronic ☐ Cert. of Mailing ☐ First Class Mail ☐ Express Mail No. 10/001,967

☒ Transmittal Letter

☐ New Patent App ☐ Utility ☐ Design ☐ CIP ☐ Div. ☐ PCT ☐ RCE ☐ Prov

☐ Other: \_\_\_\_\_

pages of Specification \_\_\_\_\_

pages of Claims \_\_\_\_\_

pages of Abstract \_\_\_\_\_

pages of Formal/Informal Drawings \_\_\_\_\_

☐ Small Entity ☐ Large Entity

☐ Declaration/Power of Attorney

☐ Recordation of Assignment/Security Agreement

☒ Information Disclosure Statement

1 Form PTO 1449

1 copies of cited references

☐ Preliminary Amendment

☐ Response to Missing Parts Notice

☐ Resp. to Notice to Correct App. Papers

☐ Certified Copy of Priority Doc.

☐ Claim for Convention Priority

☐ Response/Amendment to Office Action of \_\_\_\_\_

Request for \_\_\_\_\_ month Extension of Time

☐ Letter submitting \_\_\_\_\_ pages of drawings

☐ Req. for Approval of Drawing Amendments

☐ Req. for Oral Hearing

☐ Not. of Appeal ☐ Appeal Brief ☐ Reply Brief

☐ Rule 312 Amendment/Letter

☐ Req. for Acknowledgement of Cited Art

☐ Issue Fee

☐ Publication Fee

☐ Req. for Certificate of Correction

☐ Maintenance Fee for \_\_\_\_\_ years after grant

☐ Fee Address Indication Form

☐ Terminal Disclaimer

☐ Petition to Commissioner

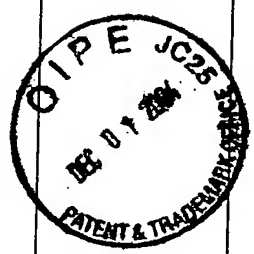
☐ Status Inquiry

☒ Other Copy of JP Office Action

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THE PATENT AND TRADEMARK OFFICE DATE STAMPED HEREON IS ACKNOWLEDGEMENT THAT THE ITEMS, CHECKED ABOVE, WERE RECEIVED BY THE PTO ON THE DATE STAMPED.



Docket No.: 60188-126

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**REMINDER  
COPY**

In re Application of

Junichi YANO, et al.

Application No.: 10/001,967

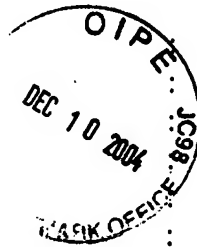
Filed: December 05, 2001

Customer Number: 20277

Confirmation Number: 1397

Group Art Unit: 2111

Examiner: Kim, Kenneth S.



For: INTEGRATED CIRCUIT WITH CPU AND FPGA FOR RESERVED  
INSTRUCTIONS EXECUTION WITH CONFIGURATION DIAGNOSIS (As Amended)

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop ISSUE FEE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This Information Disclosure Statement is being filed more than three months after the U.S. filing date and after the mailing date of a Final Rejection or Notice of Allowance, but before payment of the Issue Fee.

**REQUEST TO CONSIDER REFERENCES AFTER CLOSE OF PROSECUTION AND  
BEFORE PAYMENT OF ISSUE FEE**

The undersigned hereby requests consideration and entry of this Information Disclosure Statement and accompanying references under 37 CFR 1.97(d).

Please charge the processing fee under 1.17(p) of \$180.00 to Deposit Account 500417.

CERTIFICATION PARAGRAPH

REMINDER  
COPY

The undersigned certifies that each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement as described in 37 CFR 1.97(e)(1).

The Japanese language reference, 2000-235489, was cited in a corresponding Japanese application office action and its relevance discussed therein. A copy of the foreign office action, together with an English language version thereof, is attached for the Examiner's information.

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP

  
Michael E. Fogarty  
Registration No. 36,139

600 13<sup>th</sup> Street, N.W.  
Washington, DC 20005-3096  
Phone: 202.756.8000 MEF:tlg  
Facsimile: 202.756.8087  
Date: December 1, 2004

Please recognize our Customer No.  
20277 as our correspondence address.



SHEET 1 OF 1

INFORMATION DISCLOSURE  
CITATION IN AN  
APPLICATIONATTY. DOCKET NO.  
60188-126SERIAL NO.  
10/001,967REMINDER  
COPY

(PTO-1449)

APPLICANT  
Junichi YANO, et al.FILING DATE  
December 05, 2001GROUP  
2111

## U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Codez (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US			
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## FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes-Number + Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation Yes No
		JP 2000-235489 A	8/29/2000	HITACHI LTD		w/English Abstract and partial English Translation

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.

EXAMINER

DATE CONSIDERED

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

REMINDER  
COPY

Applicant

Junichi YANO, et al.

Docket No. 60188-126

Serial/Reg./Patent No. 10/001,967

INTEGRATED CIRCUIT WITH CPU AND FPGA FOR RESERVED INSTRUCTIONS EXECUTION WITH  
CONFIGURATION DIAGNOSIS (As Amended)

Title:

Date Sent: 9/20/2004

☒ Hand Carried

☐ Fax

☐ Electronic

☐ Cert. of Mailing

☐ First Class Mail

☐ Express Mail No.

☒ Transmittal Letter

New Patent App

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☐ Cont.

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Secy. or PL:

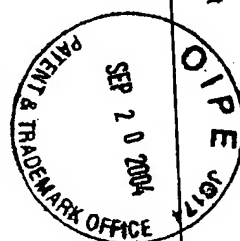
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- ☐ Req. for Approval of Drawing Amendments
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In re Application of	:	Customer Number: 20277
	:	
Junichi YANO, et al.	:	Confirmation Number: 1397
	:	
Serial No.: 10/001,967	:	Group Art Unit: 2111
	:	
Filed: December 05, 2001	:	Examiner: Kim, Kenneth S.
	:	
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Each non-English language reference, listed on the attached PTO-1449, have been cited in a corresponding Japanese application office action and its relevance discussed therein. A copy of the foreign office action, together with an English language version thereof, is attached for the Examiner's information.

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Respectfully submitted,

MCDERMOTT WILL & EMERY LLP

  
Michael E. Fogarty  
Registration No. 36,139

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